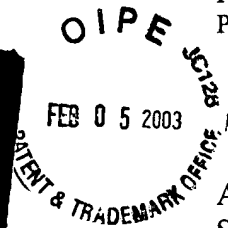


Applicant : Mojdeh Shakeri et al.
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Attorney Pocket No.: 04899-050001



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Mojdeh Shakeri et al.
Serial No. : 09/870,280
Filed : May 30, 2001
Title : BLOCK DIAGRAM MODELING

Art Unit : 2185
Examiner : Unknown

Commissioner for Patents
Washington, D.C. 20231

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Technology Center 2100

PRELIMINARY AMENDMENT

Prior to examination, please amend the application as follows:

In the specification:

Replace the paragraph beginning at page 1, line 21 with the following rewritten paragraph:

-- One or more of the following features may also be included. Each of the blocks includes at least one output signal port. A plurality of input signal values and the output signal values have at least one attribute. The attribute may be a name, a data type, a numeric value and/or a dimensionality. The ordered set is a linked list data structure. The linked list data structure is a tree data structure, the tree data structure including $m + n$ nodes. m represents a number of independent signals and n represents a number of composite signals.--

CERTIFICATE OF MAILING BY FIRST CLASS MAIL

I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, Washington, D.C. 20231.

1/28/03

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Alissa Passacantilli

Alissa Passacantilli

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